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Development of Durable 3-Electrode Lithium-Ion Pouch Cells with LTO Reference Mesh: Aging and Performance Studies

Bernd Epding, ^{1,z} Agate Broda, Björn Rumberg, Hannes Jahnke, and Arno Kwade³

Building a lithium-ion cell with a third reference electrode which is stable and does not disturb the cell characteristics promises deep insights into the cell performance and aging behavior. In this study, a lithium titanium oxide coated aluminum mesh is introduced into lab size 28 mAh pouch cells, which allows the cells to be braced as usual. The influence of inserting the coated mesh together with an additional separator is analyzed using electrochemical impedance spectroscopy and cyclic aging tests. Cells with this reference electrode show small deviations in the Nyquist plot and an increased capacity fade compared to the standard cells. Nonetheless, these 3-electrode cells already allowed C-rate performance tests in a fresh and aged cell state. According to this, the charge current profiles for fresh and aged cells are optimized as step wise protocols to improve the utilization of the anode overpotential reserve while avoiding lithium plating. In order to realize this, the aged cells require a 60% slower protocol. This work shows that the overpotentials on the anode side increase during aging, which makes the cells more susceptible to lithium plating. However, compared to a constant current charge, a reduction in charging time of 30% is possible.

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Over the last years it has been a prime goal to increase the energy density of lithium-ion cells to facilitate longer range of battery electric vehicles or longer usage times of mobile devices while maintaining the package volume. By now it has become clear that with state of the art cells 100 kWh batteries for electric vehicles can be achieved and driving ranges of over 500 km are possible. Still, to achieve maximum flexibility for the user, the battery needs to be able to be recharged quickly. With state of the art lithium-ion technology, the charge capability of a cell tends to decrease with increasing energy density. Since the current density in the cell increases for higher energy density cells, the diffusion in the thick porous electrodes limits lithium-ion transport. This in turn leads to increasing overpotentials at the anode surface which eventually result in a negative electrode potential vs. Li/Li⁺.² Whenever this is the case, the deposition of lithium as a metal film on the graphite surface becomes thermodynamically possible, so called lithium plating.3-5 Lithium plating can lead to a fast capacity loss since the metallic lithium reacts with the electrolyte to form new passivating layers or is no longer available for the electrochemical reactions due to loss of electrical contact to the graphite. If the lithium plating appears in the form of dendrites which pierce through the separator, it can lead to electrical shorts inside the battery causing an accelerated self-discharge or even thermal runaway. To be able to use current cell technologies to their maximum capabilities while keeping the operation safe and to compare different approaches to overcome their limitations, new methods are needed to precisely detect the occurrence of lithium plating. Various methods have been described in the literature to directly or indirectly detect lithium plating in lithium-ion cells.³ So far none of these methods allow the direct detection of lithium plating without modifying the cell setup or disassembling it to gather electrode material. Indirect electrochemical techniques (lithium stripping, voltage relaxation, high precision coulometry)⁶⁻⁸ can indicate that lithium plating occurred during the previous charge step. However, in order to detect the precise onset of lithium plating during a charge period, many cells have to be tested under varying external conditions. Furthermore, testing multiple conditions with the same cell reduces data quality since massive lithium deposition is required before it can be detected with indirect methods, which severely damages the cell. Also, the fast reintercalation of lithium into the graphite anode limits the plating detectability at higher temperatures which are commonly induced by fast charging. 9-11 Similar arguments can be made toward methods like neutron diffraction¹² or volume detection¹³ but here, actual production cells can be tested. More sensitive techniques like EPR14 or NMR15 still require small specialized cells to fit into the measurement equipment.

Finally, inserting a third electrode as a reference into lithium-ion cells has already proven to give great insight into their properties. With a reference electrode, anode and cathode potential can be measured independently and thus questions like proper balancing of electrodes and impedance contributions can be checked. Due to the fact that the anode potential falls below 0 V vs Li/Li⁺ before lithium plating occurs, a reference electrode can also be used to detect its onset during a charge period.^{3–5} Additionally, it gives the opportunity to stop a charge step if the anode potential reaches a critical value and thus limits the damage to the test cell. This way a single cell can be used to test a whole range of operating conditions.

The optimal way to construct a 3-electrode lithium-ion cell is still under debate. Depending on the intended measurements many possible designs exist. For electrochemical impedance spectroscopy (EIS) measurements the geometry of the cell design can introduce measurement artifacts. 16 Furthermore, the transferability of results gathered from 3-electrode cells to the actual production cells is often questionable. If the cell design has to be changed a lot to insert the reference electrode or if the reference has an inconvenient geometry and/or is placed at the wrong position inside the cell, the obtained electrochemical results do not allow precise conclusions for production cells. 17 Finally, most 3-electrode designs based on lithium metal electrodes suffer from fast degradation since lithium metal reference electrodes change or are even consumed in the cell. 18 Thus, no continuous aging information is available from 3-electrode cells but only spot tests at certain states of aging are possible. Furthermore, it is difficult to build 3-electrode cells from post-mortem material of aged cells as the electrodes often show inhomogeneous aging patterns and become brittle.⁴

In this work a 3-electrode pouch cell design is realized in which the reference is an aluminum mesh coated with lithium titanium oxide (LTO, Li₄Ti₅O₁₂) similar to what was shown by Costard et al. for small coin cell types. 19 The mesh has the same size and geometry as the electrodes and allows the 3-electrode pouch cells to be braced the same way as standard pouch cells. LTO serves as a chemically stable two phase reference material.^{20,21} This way it is possible to show the influence of the changes made to the standard cell on the aging behavior and gather continuous 3-electrode data over the whole aging campaign. C-rate tests were conducted on fresh as well as aged 3-electrode cells. The results indicate that the onset of lithium plating is heavily influenced by the aging of the electrode materials as it was predicted by Yang et al. and observed by others. 22-24 Thus, common fast charge protocols which are defined for fresh cells will likely lead to lithium plating in aged cells. Finally, a current step protocol is derived for the 3-electrode cells to determine how far the charging time can be reduced with an optimized charging protocol in fresh and aged cells.

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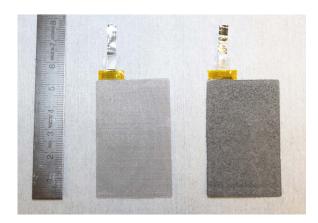


Figure 1. LTO reference electrode. Uncoated aluminum mesh (left) with current collector tap already welded on. Aluminum mesh after dip coating with LTO (right).

Experimental

Production of LTO slurry and coating on aluminum mesh.— For the reference electrodes LTO was coated onto a woven aluminum mesh (SPÖRL KG) with 52 μm thick wires and 75 μm mesh opening. The LTO slurry consisted of 25.5 g LTO powder with about 7 μm LTO particles, 1.5 g C65 conductive carbon black and 3 g Seika AG-binder dispersed in 200 ml deionized water at 2000 rpm for 15 minutes. Aluminum mesh sheets (3.25 cm \times 5.15 cm) were punched out and an aluminum current collector was welded on. The mesh was cleaned thoroughly with isopropyl alcohol and manually dip coated in the LTO slurry. Following this, the meshes were dried in air for 1 hour and excess slurry was carefully removed at the bottom of the meshes. Finally, the meshes were dried at 100°C for 2.5 hours in vacuum.

Building and characterizing coin cells with LTO mesh against lithium metal.—In order to check the equilibrium voltage as well as the capacity of the produced LTO meshes, 14 mm diameter samples were prepared as described above and transferred into an argon filled glove box. With these electrodes CR2032 coin cells were built using a lithium metal counter electrode, two layers of a Celgard 2500 separator and 70 μl standard electrolyte containing carbonate solvents and LiPF₆.

The coin cells were discharged with a current of 50 μA until a cutoff voltage of 1.3 V was reached using a BaSyTec CTS test bench. Afterwards, the cell voltage was monitored over a 12 day period. Testing was performed at room temperature (23°C \pm 1°C).

Building and characterizing pouch cells with and without LTO reference mesh.—Lab-size nominal 28 mAh pouch cells were fabricated using one-side machine coated electrodes for PHEV applications from the Volkswagen-Varta Microbattery joint venture. The electrodes employ NMC622 on the cathode side and graphite on the anode side. Sheets of 3.1 cm \times 5 cm for the cathodes and 3.25 cm \times 5.15 cm for the anodes were punched out of the electrode coils. Small areas at the top of each electrode were cleaned manually from active material and current collector tabs were welded on via ultrasound. The electrodes were dried overnight in vacuum at room temperature and subsequently at 100° C (anodes) or 150° C (cathodes) in vacuum for 5 hours before cell assembly.

Care was taken to prevent a short between the cathode and the reference electrode by isolating the current collector tabs with Kapton tape on the inside of the cell (see Figure 1) and with scotch tape on the outside since both taps exit the cell at the same position to make sealing easier. After the assembly of the various cell configurations with separators (W-Scope) shown in Figure 2, the cells were dried again at 60° C in vacuum overnight. Following this, the cells were transferred into an argon filled glove box, injected with $280-350 \,\mu l$ (depending on the number of layers) of a standard electrolyte containing carbonate solvents and LiPF₆ and finally sealed. After tempering at 60° C for

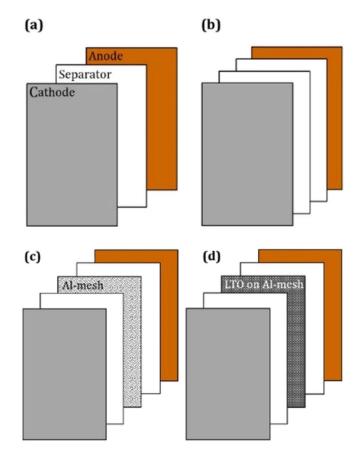


Figure 2. Incremental changes to the pouch cell design. Cells were built with: a) one layer of separator (standard cell), b) two layers of separator, c) an uncoated aluminum mesh between two layers of separator, d) an aluminum mesh coated with LTO between two separators between the anode and the cathode.

2 hours the cells were degassed and sealed again. The finished pouch cells were braced in plastic fixtures (0.4 Nm torque) and connected to a BaSyTec CTS test bench for formation.

All electrical tests involving the 3-electrode cells and any electrochemical impedance spectroscopies were performed using a Biologic VMP3 potentiostat. Cycling of cells without a reference electrode was done using the BaSyTec CTS test bench. The cells were analyzed either at room temperature (23°C \pm 1°C) or in a CTS climate chamber (45°C \pm 1°C).

Formation consisted of a C/20 charge step to 4.15 V followed by a C/20 discharge for 5 hours, C/20 charge to 4.15 V and again 5 hour C/20 discharge. This was followed by a C/10 charge to 4.15 V, a C/10 discharge for 5 hours, C/10 charge to 4.15 V and then C/10 discharge to 3.0 V to measure the initial capacity. Following this, a C/3 charge to 4.15 V with a constant voltage (CV) phase to C/20 and a C/3 discharge without CV phase to 3.0 V were performed. The formation ended with a C/3 charge to 3.6 V (about 20% SoC) to avoid an unintended deep-discharge. Between every charge and discharge step a 10 min rest period was applied. Using formation cycles at high cell voltages instead of full cycles allowed us to shorten the procedure and to form a stable, compact solid electrolyte interface (SEI) from the beginning.²⁵

After the cell formation, the reference was conditioned by connecting the reference electrode to the cathode. The reference potential was set by charging 1 mAh, which is about one sixth of its theoretical capacity, into the LTO electrode with a current of 0.5 mA to reach the stable two phase voltage plateau.

Aging cycles were conducted at 45° C. The cells were charged with 3C to 4.15 V followed by a CV phase to C/20. After 10 min rest time the cells were discharged with 3C to 3.0 V without CV phase followed again by 10 min rest time.

Table I. Initial capacity and characteristic resistance of all built cell types as mean values with corresponding corrected standard deviation.

Cell type	Initial C/10 capacity [mAh]	Initial characteristic resistance $[\Omega]$
Standard (6 cells)	31.74 ± 1.25	1.20 ± 0.04
Double separator (3 cells)	32.69 ± 0.26	1.45 ± 0.08
Uncoated Al-mesh (3 cells)	32.54 ± 0.32	1.50 ± 0.06
3-electrode (6 cells)	32.07 ± 0.71	1.69 ± 0.04

In order to evaluate the state of health of all the cells, a check-up routine at 23°C was performed after formation, before and after conducting the electrochemical tests and after 250 respectively 500 aging cycles. First, at a cell SoC of 20%, the LTO electrodes were conditioned again by discharging the LTO against NMC to a voltage of 0.8 V and recharging 1 mAh into the LTO electrode. Then, three full 1C cycles were performed with a CV phase only in charge direction until a current of C/20. Between every charge end discharge step a 30 minute rest period was applied. After the last 1C constant current (CC) discharge the cells were charged with C/3 to 3.6 V (about 20% SoC). Following a rest period overnight, electrochemical impedance spectra were measured in a frequency range between 10 mHz and 100 kHz.

The fast charge capabilities of the cells were measured using asymmetric C-rate tests at 23°C. In this case the charge current was varied (C/5, C/2, 1C, 2C, 3C, 4C) while the discharge current was always 1C. Three identical cycles were performed before the next charge current was applied. The charge step was terminated when the maximum voltage of 4.15 V was reached. No CV phases were used so that the cells were not damaged unnecessarily. Between every charge and discharge step a 30 minute rest period was applied. For the 3-electrode cells, the charge step was additionally terminated if the anode potential reached $-1.562\ V\ vs.\ LTO$ (respectively 0 V vs. Li/Li+) to avoid damage from lithium plating.

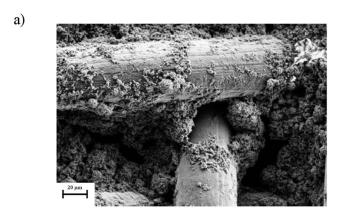
Results and Discussion

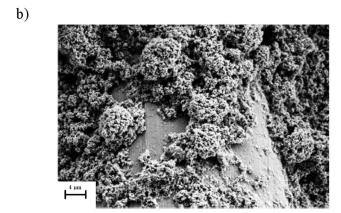
Quality of the LTO reference mesh.—The suitability of the produced LTO electrodes as reference electrodes was studied by the coin cell measurements against lithium metal and by scanning electron microscope (SEM) imaging (see Figures 3a and 3b). Coin cell experiments demonstrated a good agreement of the LTO electrode capacity with theoretical values derived from weight measurements. Furthermore, the stable potential of 1.562 V vs Li/Li⁺, as shown in Figure 3c, is in good agreement with LTO potential values published by others. 19,21 From the SEM images it becomes clear that the coarse LTO particles developed for standard electrode manufacturing are not optimal for coating a reference mesh. Most of the mesh openings are filled with LTO particles while the outside surfaces of the aluminum wires are hardly coated. However, at higher magnification the porous structure of the LTO becomes visible which allows lithium to readily diffuse through the mesh structure. Since most of the LTO is inside the mesh openings, the thickness of the mesh does not increase much further and an even electrode is obtained which does not damage the separator. Still, the use of LTO nanoparticles, as demonstrated by Costard et al., should be preferred for future experiments.¹⁹

Reproducibility of pouch cell manufacturing.—Several cells were built for each cell type (see Figure 2). To demonstrate reproducibility, Table I shows the C/10 (2.8 mA) cell capacity after formation for each type as well as their initial characteristic resistance (the local minimum in the Nyquist plot) derived from EIS measurements shown in Figure 4a.

Incremental changes to cell design.—The influence of the changes to the standard cell design as introduced by the insertion of the LTO reference mesh between the anode and cathode was evaluated by EIS measurements. In Figure 4 the EIS results for standard cells, cells with two separator sheets, cells with two separators and an uncoated

aluminum mesh and complete 3-electrode cells with LTO coated aluminum meshes are compared. Figure 4a shows the impedance spectra in a Nyquist plot. An increasing shift of the spectra to higher real impedance values is observed when inserting additional layers between the anode and the cathode. This corresponds to an increasing internal resistance due to longer diffusion paths in the electrolyte. 26,27 The small spread of the spectra for nominally identical cells is caused





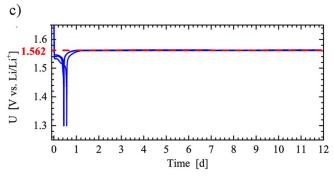


Figure 3. a),b) SEM pictures of LTO coating on Al-mesh. c) Potential of LTO coated Al-meshes during lithiation and relaxation in a coin cell against a lithium metal electrode. The cells are discharged (corresponds to lithiating the LTO electrodes) until 1.3 V. Afterwards no current is flowing and only the cell voltage is measured.

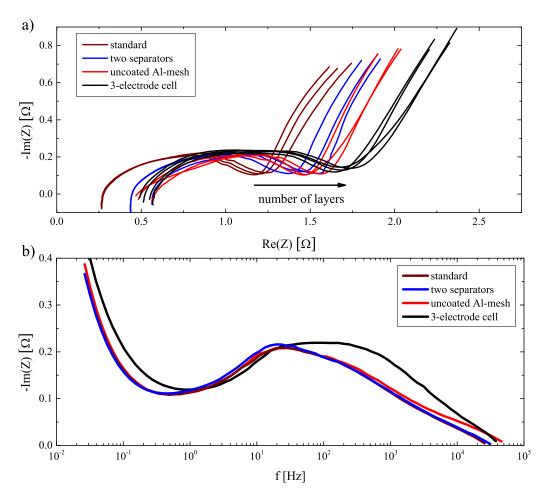


Figure 4. a) Impedance spectra of all tested cell designs in a Nyquist plot obtained at 3.6 V cell voltage (about 20% SoC). b) Plot of the imaginary part shown in a) vs. frequency to underline the deviation of the 3-electrode cells at higher frequency.

by the manual fabrication. Still, the shape of the spectra in Figure 4a stays mostly the same for all cells indicating otherwise unchanged cell dynamics. This is additionally indicated by the very similar imaginary impedance curves in Figure 4b. Only the LTO 3-electrode cells show a small deviation from the rest in the frequency range from 100 Hz to 10 kHz. Since the 3-electrode cells only differ in the LTO coating from the cells with uncoated aluminum meshes, this difference must be introduced by the LTO coating. In this frequency range capacitive effects dominate the impedance spectra so that additional electrochemical double layers might cause this deviation.²⁷ Furthermore, Costard et al. have proposed a second lithium diffusion path between anode and cathode: A part of the lithium-ions do not travel through the pores of the reference electrode but are intercalated on the one side of the LTO coating and deintercalated on the other side. 19 To limit this influence the use of a more open mesh with less LTO in the form of a very thin layer is planned. Nonetheless, with the present 3-electrode cells a detailed analysis of the cell materials and operating parameters is feasible.

Aging behavior of modified cells vs. standard cells.—The influence of the LTO reference electrode on the cell aging behavior was characterized by accelerated cyclic aging experiments. To this end, three standard cells and six 3-electrode cells were cycled at 45°C with 3C full cycles between 3 V and 4.15 V. Before the cycling started and after 250 and 500 cycles the condition of each cell was characterized using a check-up test as described above.

Figure 5 shows the capacity retention of the cells normalized to the initial capacity (state of health, SoH) as determined during the check-ups last 1C discharge. The absolute capacity of the 3-electrode

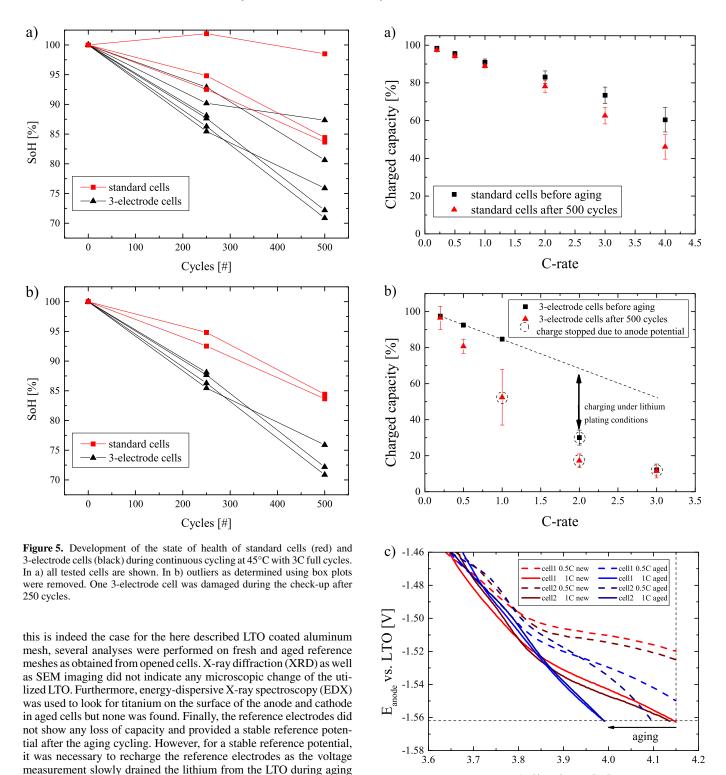
cells is about 1 mAh smaller since part of the lithium is bound in the LTO to provide a stable reference potential. Additionally, the higher internal resistance of the LTO cells decreases the measured capacity as the voltage limits in charge and especially in discharge direction are reached earlier.

Already after 250 cycles the cell capacities start to scatter. It is known that the reproducibility of manually built lab cells is not perfect. Thus, a few outliers were expected. An analysis using box plots shows that one standard cell and two 3-electrode cells behave anomalously. These cells are not included in any further analyses.

Figure 5 demonstrates that the 3-electrode cells lose more capacity over the course of the aging cycles. After 500 cycles the standard cells perform better and maintain about 85% of their initial capacity. The 3-electrode cells retain about 75% of their initial capacity. This is likely caused by a higher diffusion resistance in the 3-electrode cells due to the longer distance between anode and cathode compared to the standard cells. A longer diffusion path in the electrolyte can lead to low lithium ion concentrations on the anodes surface during charging, which increases the charge transfer overpotential and thus the risk of lithium plating. Furthermore, an inhomogeneous compression of the cell due to the dip coated reference as well as interactions of the electrodes with the LTO reference material are possible causes. However, these influences are difficult to determine. Further decreasing the thickness of the reference mesh and making the LTO coating more homogenous will likely reduce the observed differences.

Stability of the LTO reference mesh.—A main goal of this study was to find a stable reference electrode which can remain inside the lithium ion cell while it undergoes aging tests. In order to verify that

cycling.



Charging capability: new cell vs. aged cell.—The capability to be charged quickly is tested using asymmetric C-rate tests where the cells were always fully discharged with the same current (1C) and then charged with varying currents. Due to the increasing overpotentials at higher currents, the charge throughput decreases since the cutoff voltage is reached earlier. Figure 6a shows the reachable SoC with varying constant currents when starting the charging process at 0% SoC for a standard cell. 100% SoC marks the 1C discharge capacity after a 1C charge step with CV phase to C/20 as determined in the

preceding check-up. An almost linear decrease of the reachable SoC

Figure 6. Achievable SoC during constant current charging until 4.15 V in standard cells a) and in 3-electrode cells b). For the 3-electrode cells the charging was additionally terminated if the anode potential fell below -1.562 V vs. LTO. Charge steps which were terminated by the anode potential are marked with a circle. In c) the anode potential curves are plotted over the full cell potential of a 3-electrode cell before and after aging cycles.

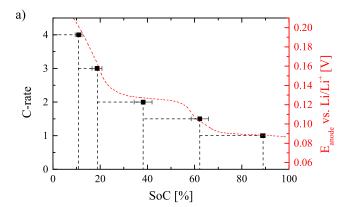
Cell voltage [V]

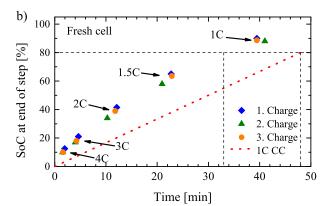
with rising C-rate before and after aging is observed. This is expected as the internal resistance of a cell can be described as a series of RC elements. At some point during continuous charging all capacities are fully charged and a series of ohmic resistances remains. After 500 aging cycles the achievable SoC for charging currents higher than 1C are slightly lower than for the fresh cell indicating an increased ohmic resistance.

For the 3-electrode cells another charge step termination criterion for the anode was introduced at -1.562 V vs LTO (0 V vs. Li/Li⁺) in order to avoid lithium plating and thus additional damage. As shown in Figure 6b, this creates quite a different picture than for the standard cells. For a fresh 3-electrode cell the CC phase is terminated by the maximum cell voltage of 4.15 V up to a C-rate of 1C. When charging with a current of 2C or higher, the charging is terminated due to the anode potential reaching the lithium plating criterion. Thus, only about 20% SoC is reached at 2C. Linearly extrapolating the data up to 1C in Figure 6b (dashed line) demonstrates that at 2C the cell could still be charged up to 70% SoC until the maximum cell voltage would be reached. However, from 30% SoC until 70% SoC at least a part of the charge would lead to lithium plating on the anode. After 500 aging cycles the critical charging rate, where the charge step is terminated by the full cell voltage and not by the anode potential, drops to C/2. In contrast to a fresh cell the aged cell cannot be charged with 1C to 4.15 V without causing lithium plating. This is demonstrated in Figure 6c where the anode potential is plotted against the full cell potential for two 3-electrode cells in a fresh and aged state. Thus, the anode reaches the plating criterion while the cell voltage lags behind. An increasing susceptibility to lithium plating with aging has been theoretically predicted by Yang et al. who explained this with decreasing anode porosity due to SEI growth.²² Schuster et al. have speculated that aging under harsh conditions can lead to lithium plating even at moderate conditions and finally to selfenforcing capacity loss. ²⁸ The C-rate tests shown here seem to confirm those theories. This leads to the conclusion that fast charging protocols which are developed for fresh lithium-ion cells explicitly to avoid lithium plating might suddenly produce lithium plating when the cell is aged.

Optimized charging procedure based on anode potential.—The aim of a fast charging protocol for any cell should be to minimize the charging time to a certain SoC while avoiding additional damage. Apart from temperature limits which must not be exceeded during cell operation, lithium plating is the main limitation for fast charging protocols for lithium-ion cells. 9-11,29,30 The 3-electrode cells built here were used to investigate how much faster they can be charged under optimal current control conditions if compared to a standard CC-CV charging procedure. In order to achieve this, a step wise charging procedure was applied for the 3-electrode cells. According to this, the cells were charged at 23°C from 0% SoC with 4C until the anode potential reaches 0 V vs. Li/Li+ or if the maximum full cell voltage of 4.15 V is reached. After a 1 minute rest period to allow the cells to relax from higher overpotentials of the previous charge step with higher current, the charging continues with 3C and so on. The applied current steps were 4C, 3C, 2C, 1.5C, 1C and 0.5C. Figure 7a shows which SoC is reached in a fresh cell with every C-rate before the rest period starts. As the charging steps at high current are all terminated due to the anode potential reaching 0 V vs. Li/Li⁺, the anode potential curve from a C/20 CC charge step is included, which was recorded before the step wise charging, on the right y-axis in Figure 7a. This seems appropriate since the overpotentials in a cell are approximately proportional to the internal cell resistance for continuous charging multiplied by the charge current according to Ohms law. A good correlation of the termination of the charging steps with the nearly OCV characteristic of the anode is observed. Thus, the step wise charging procedure appears to utilize the overpotential reserve (the difference between the anode open circuit voltage and 0 V vs. Li/Li⁺) that the anode provides well.2

Figure 7b shows the time it takes to reach a certain SoC with a 1C CC charge (red dotted line) and with step wise charging. The step





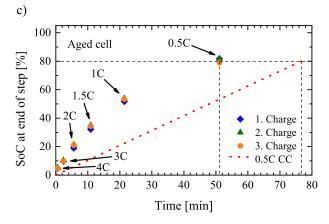


Figure 7. Step wise charging of 3-electrode cells. Current steps of a fresh cell are shown in a) as well as the anode potential from a C/20 CC charge. In b) the time it takes to charge to a certain SoC is shown for all charging steps for a fresh cell. In c) the same graph is shown for an aged cell. The red dotted lines show the charging curves of a 1C CC charge in b) respectively a 0.5C CC charge in c) as a comparison. Dashed lines are included to guide the eye. Results are shown for three consecutive charging steps. Also note the different x-axis in b) and c). An overview over the achieved charging times is given in Table II.

wise charging is compared to a 1C CC charge as 1C is the maximum constant current which allows the cell to be charged from 0% SoC to 80% SoC without switching to constant voltage phase. An SoC of 80% is chosen as the target SoC for the fast charging protocol as it is often communicated by electric car manufacturers. In the case of the 3-electrode cells, a reduction in charging time to 80% SoC from 48 min to approximately 33 min was achieved when charging with the step wise charging protocol compared to 1C CC charging protocol. This

Table II. Overview of achievable charging times to 80% SoC in fresh and aged 3-electrode cells during step wise charging or constant current charging with maximum current without lithium plating.

Charging time to 80% SOC	fresh cell [min]	aged cell [min]
Step charging	33	51
Constant current	48	77

corresponds to a reduction of about 30%. A summary of the achieved charging times is shown in Table II.

Further improvements in charging time without additional damage are only achievable by increasing the temperature during charging to speed up cell kinetics or by improving the cell chemistry. In terms of current control strategy, we believe a step wise charging which maximizes the utilization of the anode potential curve at every given temperature, is the best possible way. Moreover, if a shorter charging time or lower SoC is desired, the step wise charging protocol is even more advantageous, e.g. after a charging time of 20 min the SoC can approximately be doubled.

In order to demonstrate the influence of increasing overpotentials on the anode side due to aging, Figure 7c shows the results of the step wise charging on an aged cell. The charging to 80% SoC (relative to remaining capacity) with the step wise charging protocol now takes about 51 minutes. Another charging step with 0.5 C is needed to reach the desired SoC as the foregoing 1C is terminated early at only 55% SoC as the anode potential reaches 0 V vs. Li/Li⁺. Still, the step wise charging protocol is much faster than a 0.5 C constant current charge which takes about 77 minutes to reach 80% SoC (see Table II). Also after a charging time of 20 min the difference is more pronounced as for the fresh cell: With the step wise charging protocol a SoC of more than 50% can be achieved compared to about 20% of 1C CC charging.

This work shows that special care has to be taken when defining such fast charging procedures. Safety margins which take the aging of the cell into account should be included as this would increase cell lifetime. Alternatively, the charging protocol has to be dynamically adapted to the state of health.

Conclusions

In this study 3-electrode cells are proving to be a valuable tool to investigate and understand state of the art lithium-ion cells. Aluminum meshes coated with LTO allow the production of durable pouch cells which can undergo performance and aging tests. However, EIS and aging investigations show that a direct comparison of these 3-electrode cells with standard cells is challenging. The longer distance between anode and cathode as well as the porous LTO coating increase the internal resistance of the cells. This leads to accelerated aging of the 3-electrode cells. In order to improve the transferability of results gathered from 3-electrode cells, the thickness of the additional layers should be reduced as well as the coating of the aluminum mesh. A sparse expanded aluminum mesh coated with LTO nanoparticles and very thin separators should allow a better transfer of results from the 3-electrode to the standard cells.

Here, the focus were fundamental performance studies. C-rate tests were used to deduce a step wise charging protocol which optimizes the use of the anode overpotential reserve while avoiding additional damage due to lithium plating. A reduction of the charging time of about 30% could be achieved to reach 80% SoC for fresh cells compared to a standard constant current charge. However, aged 3-electrode cells show an increased susceptibility to lithium plating as the anode overpotentials increase during aging. Using the protocol which was optimized for the fresh cells would lead to lithium plating in aged cells. Thus, the step wise charging protocol had to be adapted for the aged cells. With the adapted step wise charging protocol the

achievable SoC can be more than doubled after a charging time of 20 min

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